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## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

ATTY. DOCKET NO.	SERIAL NO.
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Daniel Eddleman	
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PADEMARY			U.S. PATENT DOC	IMPNTS				
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE		
		F	OREIGN PATENT D	OCUMENTS				
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C.A.	Galinski, Martin; "Circuit manages power-up sequencing"; EDN; October 31, 2002.							
C.A.	Linear Technology; "LT1645 Dual-Channel Hot Swap Controller/Power Sequencer"; Datasheet; 1999.							
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C.A.	Maxim Integrated Products; "MAX5039/MAX5040 Voltage-Tracking Controllers for PowerPC, DSPs, and ASICs"; Datasheet; May 2002.							
C.A.	Summit Microelectronics, Inc.; "SMT4004 QUAD TRAKKING™ POWER SUPPLY MANAGER", Datasheet; June 9, 2003.							
C.A.	Summit Microelectronics, Inc.; "SMT4004 QUAD TRACKING POWER SUPPLY MANAGER ADVANCED CURRENT SENSING SCHEMES AND POWER MOSFET SELECTION"; Application Note 20; February 21, 2002.							
C.A.	Summit Microelectronics, Inc.; "SMT4004 QUAD TRACKING POWER SUPPLY MANAGER Windows GUI Users Guide and Configuration Register Descriptions"; Application Note 22; August 23, 2002.							
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C-A.	Summit Microelectronics, Inc.; "Xilinx Virtex <sup>TM</sup> -E, Spartan <sup>TM</sup> -IIE FPGA and SMT4004 TRAKKER <sup>TM</sup> Supply Manager Reference Design: Procedure and Results Summary"; Application Note 31; January 7, 2003.							
C.A.	Summit Microelectronics, Inc.; "Lossless Tracking Procedure and Results Summary Reference Design: Xilinx Virtex <sup>TM</sup> -E, Spartan <sup>TM</sup> -IIE FPGA and SMT4004 TRAKKER <sup>TM</sup> "; Application Note 34; January 7, 2003.							

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DATE CONSIDERED

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.